

EC262 Problem Set 4

Due: Friday 7 September 2012

Complete the following problems from the textbook *Digital Design with an Introduction to the Verilog HDL*, Mano and Ciletti, Fifth Edition.

Chapter 2

Problems: 2(bcd), 3(a,c,d), 9(a,b), 15, 17(c), 18, 22(a).