

Homework 7

1. What is the interrupt number for EINT1?

35

2. What is the PC value immediately after a watchdog timer interrupt?

0x40

3. What pin is EINT2 located at? (port and bit)

P2.12

4. Where in memory is the interrupt flag bit for SPI?

0x4002001C Bit 0

5. Write assembly code to enable EINT1.

```
//0xE000 E100 bit 19
    movw r6, #0x0000 // bit 19 set mask
    movt r6, #0x0008
    movw r8, #0xE100 // enable register
    movt r8, #0xE000
    ldr r9, [r8]
    orr r9, r6
    str r9, [r8]
```