

Homework 18 Solutions

```
LIBRARY ieee;
USE ieee.std_logic_1164.all;

ENTITY nand_func IS
    PORT (a, b, c : IN std_logic;
          d : OUT std_logic);
END nand_func;

ARCHITECTURE d_out OF nand_func IS
BEGIN
    d <= NOT( a AND ( (a AND b) OR NOT c));
END d_out;
```