

PRE-LAB

Design and implement (using Altera) a 4-bit binary full-adder (includes carry-in). Hint: you can create a 1-bit adder, create a symbol within Quartus for that schematic, and then use four of those adders to complete this task. (Note: Use separate directories for the 1-bit adder and the 4-bit binary full adder.)

LAB

Implement the 4-bit binary full adder on your board. Demo this functionality to your instructor.