EE313 SYLLABUS

Wk	Day	Lesson	Topic	Reading Due (section #)	HW		Lab
			Week of 5 Jan				
1	M						
	W	1 & 2	**Interfacing w/ the Analog World & Numbering**	**1-1 to 1-6**	PS 1/2/3		
	Th	3	**Numbering & Codes**	**1-7 to 1-13 & Course Policy**			
	F	4	**Waveforms**	**2-1 to 2-4**	PS 4/5		
			Week of 12 Jan				
2	M	5	**Quiz #1**				
	W	6	**Logic Gates I**	**3-1 to 3-11**	PS 6		
	Th		L1: Intro to Workstation				LAB #1
	F	7	**Logic Gates II**	**3-1 to 3-11**	PS 7		
			Week of 19 Jan				
3	M		**Martin Luther King Day**	**No Classes**			
	Tu	**Monday Schedule**	**Quiz #2**				
	W	8	**Boolean Algebra I**	**5-1, 5-2, 5-5**	PS 8		
	Th		**L2: Combinational Logic**	**4-4**			LAB #2
	F	9	**Boolean Algebra II**	**5-3 to 5-7**	PS 9		
			Week of 26 Jan				
4	M	10	**Combinational Logic**	**5-7, 5-8**	PS 10		
	W	11	**K-Map Design I**	**5-9**	PS 11		
	Th		**L3: Combinational Logic Design**				LAB #3
	F	12	**K-Map Design II**	**5-9, 5-10**	PS 12		
			Week of 2 Feb				
5	M	13	**XOR/XNOR, Parity Circuits**	**6-1 to 6-4**	PS 13		
	W	14	**Digital Arithmetic**	**7-1 to 7-5**	PS 14		
	Th		**Quiz #3 Extra Lab Period**				
	F	15	**Arithmetic Circuits**	**7-6 to 7-9**	PS 15		
			Week of 9 Feb				
6	M		**Review for Exam #1**				
	W		**Review for Exam #1**				
	Th		**EXAM #1**				
	F	16	**Comparators/Decoders/Encoders**	**8-1 to 8-9**	PS 16		

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			Week of 16 Feb				
	M		**Presidents Day**	**No Classes**			
	W	17	**Flip-Flops I**	**10-1 to 10-5**	PS 17		
	Th		**L4: FLIP-FLOP**				LAB #4
	F	18	**Flip-Flops II**	**10-6 to 10-8**	PS 18		
			Week of 23 Feb				
8	M	19	**Analyzing Sequential Circuits**	**12-1**	PS 19		
	W	20	**Flip Flop Applications**	**12-2 to 12-9 & 13-1 to 13-9**	PS 20		
	Th		**Extra Lab Period**				
	F		**Flip Flop Applications**	**12-2 to 12-9 & 13-1 to 13-9**			
			Week of 2 Mar				
9	M	21	**Asynchronous Counters**	**12-2 to 12-10**	PS 21		
	W	22	**Asynchronous Counters Design I**	**12-2 to 12-10**	PS 22		
	Th		**L5:**				LAB #5
	F	23	**Asynchronous Counters Design II**	**12-2 to 12-10**			
			Week of 9 Mar				
10	M	24	**State Machine Design**	**12-2 to 12-10**	PS 23		
	W	25	**State Machine Design**				
	Th		**Extra Lab Period**				
	F						
			Week of 16 Mar - SPRING BREAK - NO CLASS				
			Week of 23 Mar				
11	M	26	**Registers: P-S, S-P, Shift & Recirculating**	**13-1 to 13-8**			
	W		**Registers: P-S, S-P, Shift & Recirculating**	**12-2 to 13-10**			
	Th		**Extra Lab Period**				
	F	27	**ADC/DAC**	**15-1 to 15-11**	PS 24		
			Week of 30 Mar				
12	M		**Review for Exam 2**				
	W		**Review for Exam 2**				
	Th		**Exam #2**				
	F		**ADC/DAC**	**15-1 to 15-11**			

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Week of 6 Apr							
13	M	28/29	**Memory I/II**	**16-1 to 16-6**	PS 25/26		
	W	31	Microprocessor Organization &Fundamentals (power point)	**17-1 to 17-3**	PS 27		
	Th		**L6:**				Lab #6
	F	32	Instruction Execution w/in a Microprocessor (power point)	**17-4 to 17-7**	PS 28		
Week of 13 Apr							
14	M	33/34	Microcontroller Organization (power point)	**18-1 to 18-6**	PS 34		
	W	35	**ASICs, CPLDs, & FPGAs**	**4-1 to 4-3, 4-5 & Handout**	PS 35		
	Th		**LAB 6**				
	F		**LAB 6**				
Week of 20 Apr							
15	M		**LAB 6**				
	W		**LAB 6**				
	Th		**Review for Final**				
	F		**Review for Final**				
Week of 27 Apr							
16	M		**Close out**				

- Readings to be completed prior to the lesson.
- Homeworks are based on the lesson that day and reading from the previous night

Text: All readings are from **Digital Electronics with VHDL, Quartus II Version by William Klietz**