

17-15

- 1) READS MEMORY LOCATION 2000H storing the instruction in the instruction register decoder.
- 2)3) READS MEMORY LOCATIONS 2001H + 2002H. STORING THE CONTENTS 4000H IN THE ADDRESS LATCH.
- 4) READS THE CONTENTS OF THE ADDRESS STORED IN THE ADDRESS LATCH AND STORES IT IN THE ACCUMULATOR.

17-16

- 1) CLOCKS THE OUTPUT D-FLIP FLOP THATS STORING THE CONTENTS OF THE ACCUMULATOR IN THE OUTPUT D FLIP-FLOP. OF FIG. 17-1.