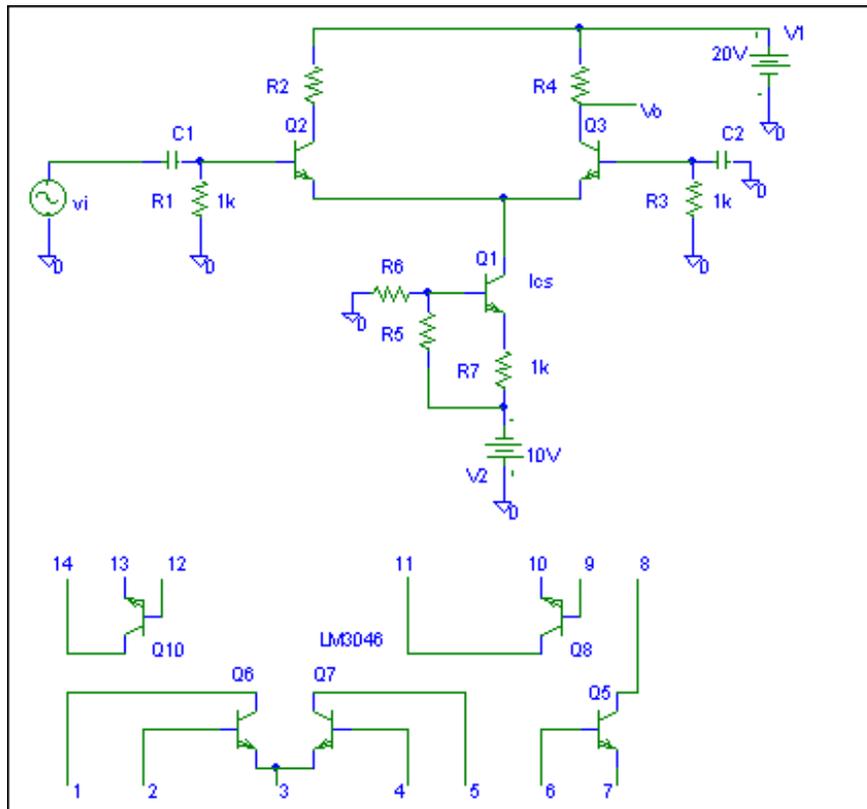


EE 342 INTEGRATED-CIRCUIT DIFFERENTIAL S99 AMPLIFIER LAB

Consider the circuit:



1. Choose R_5 and R_6 to make $I_{CS} = 2\text{mA}$.
2. Choose R_2 and R_4 to make $V_{C2} = V_{C3} = 10\text{V}$.
3. Measure v_o/v_i and compare to theoretical. (Let $C_1 = C_2 = .1\mu\text{F}$)
4. Repeat for $I_{CS} = 1\text{mA}$. with your choice of collector bias.
5. Add another stage of gain driven from the collector of Q_3 using your own design.
6. Compare all experimental and theoretical data.

Note: (1) Pin 13 on the LM3046 must be connected to -10V
 (2) There is a small sunken dot on the top of the chip near pin 1