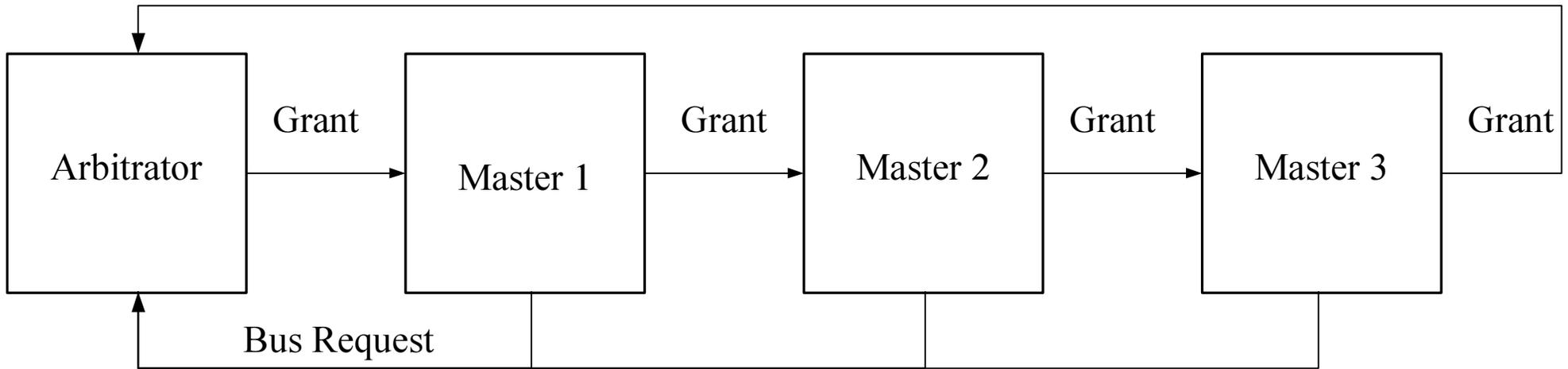
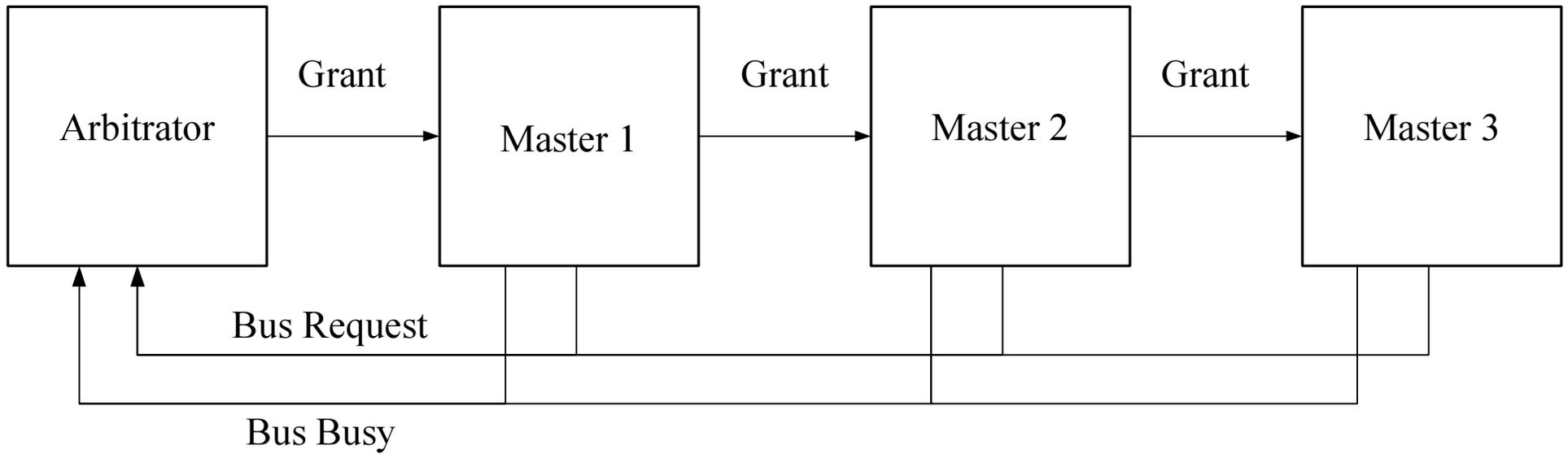


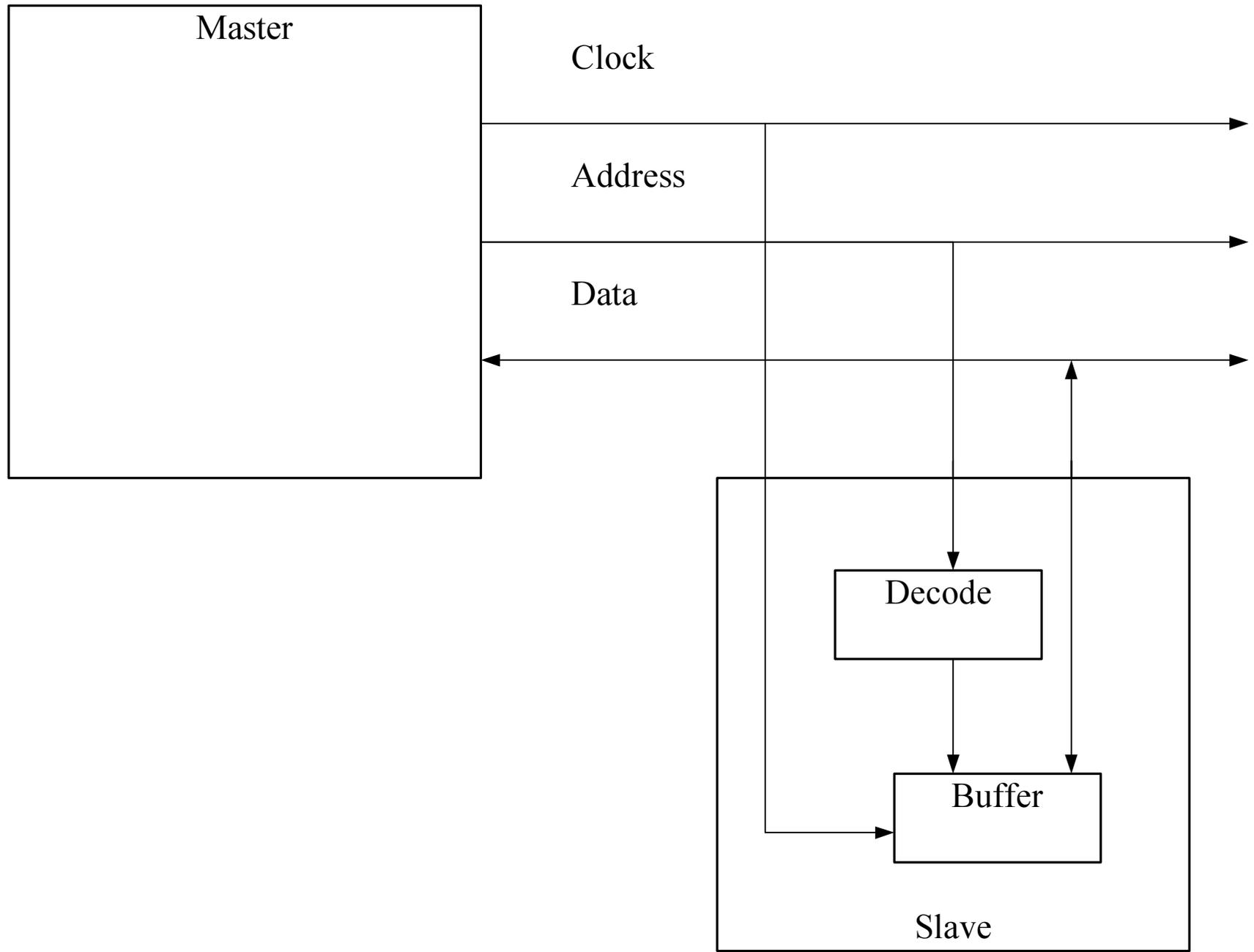
Memory Buses



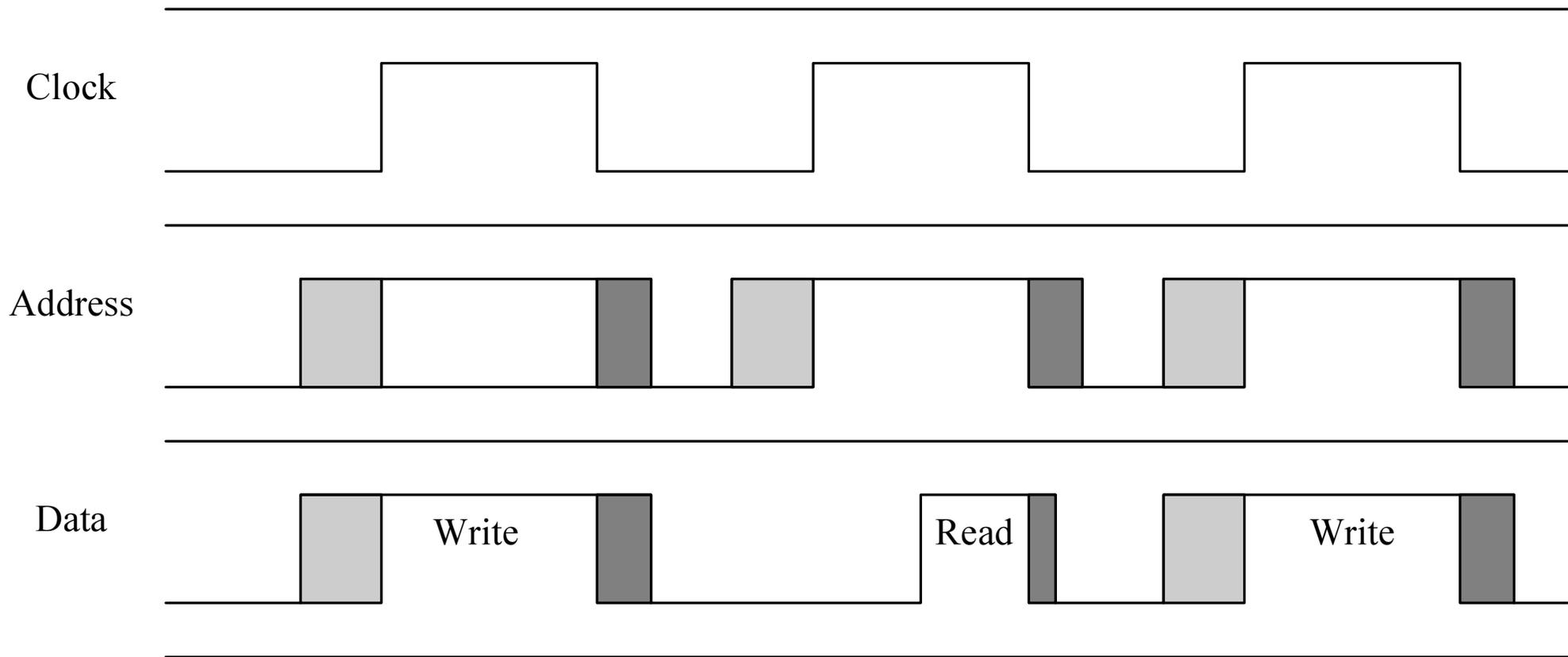
Bus Arbitration  
Using a Daisy Chain

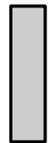


Bus Arbitration  
Using a Daisy Chain  
(Single Grant)



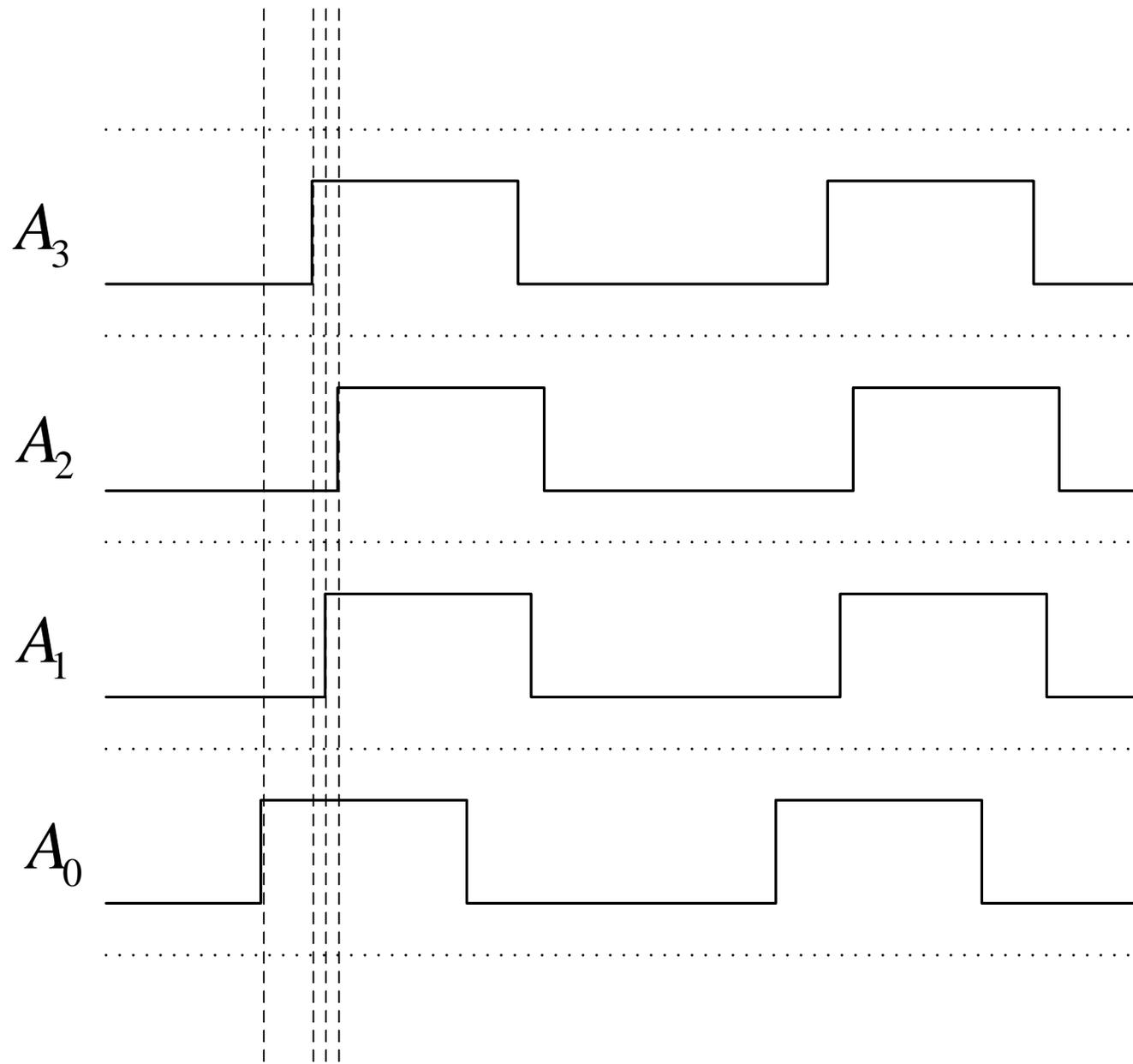
Synchronous Handshaking



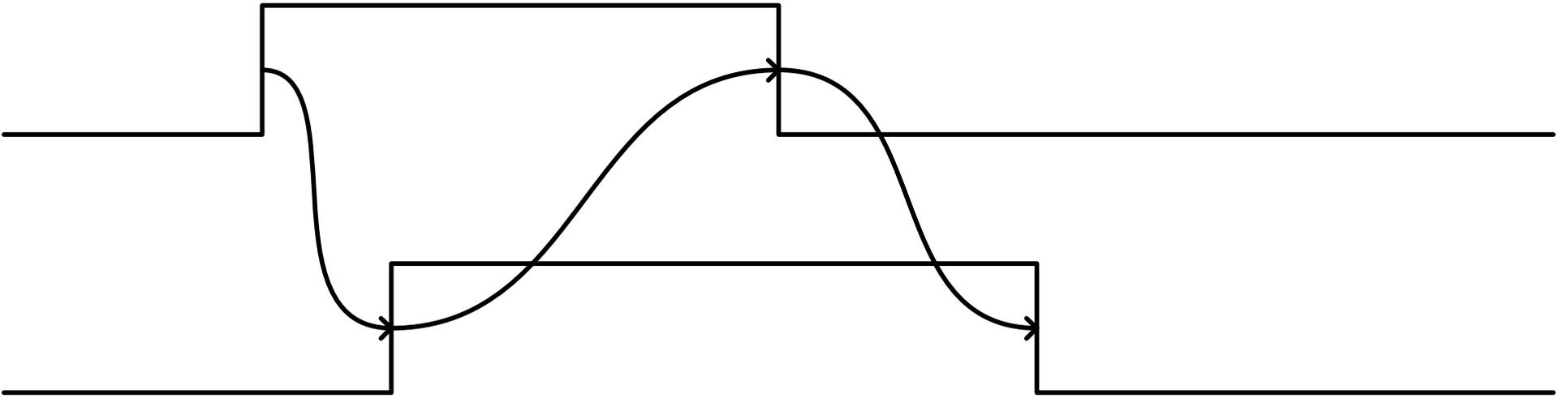
 Skew, Decode, & Setup Time

 Hold and Skew Time

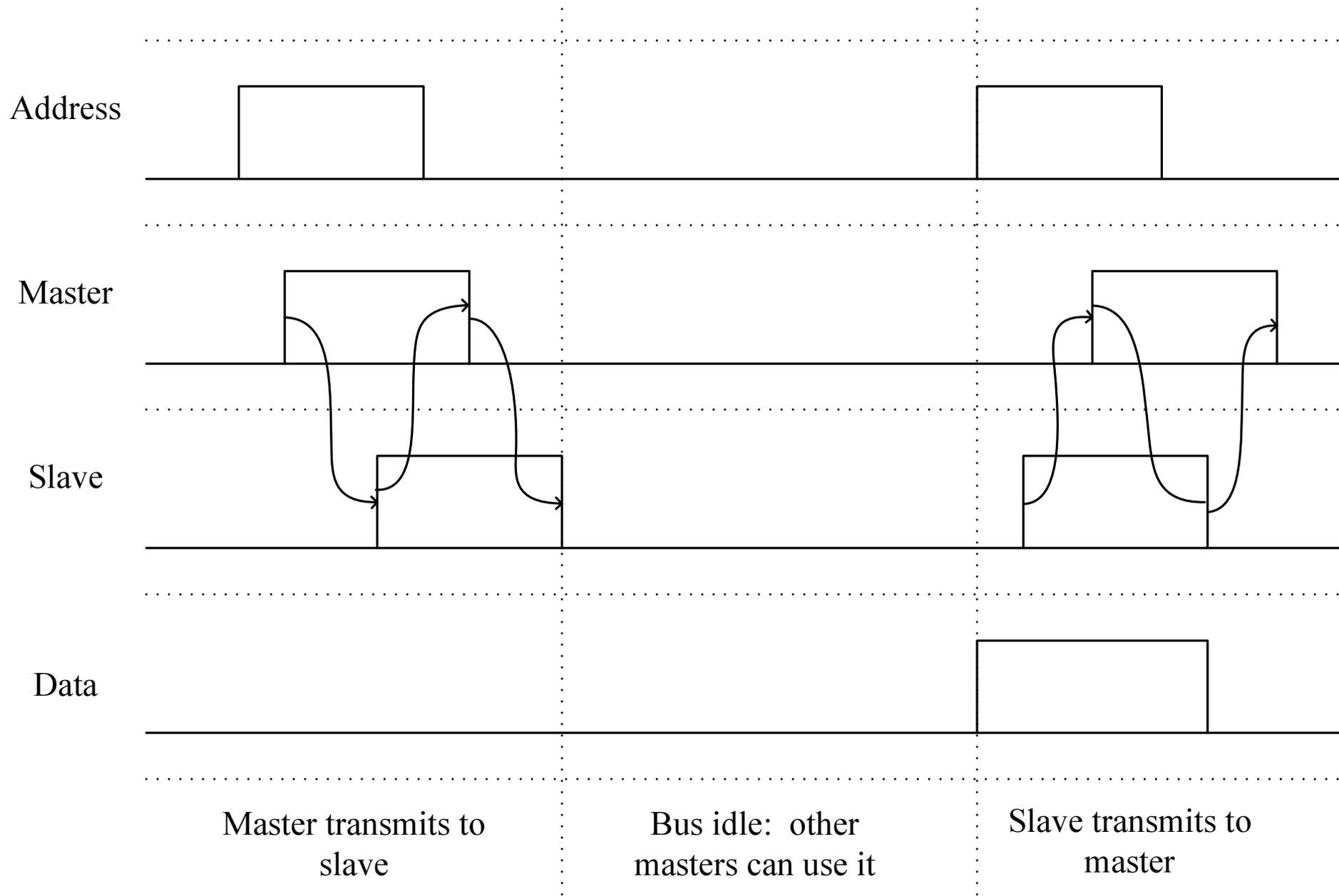
## Timing for Synchronous Handshaking



Signal Skew



Asynchronous  
Handshaking



Split-Cycle Bus Timing