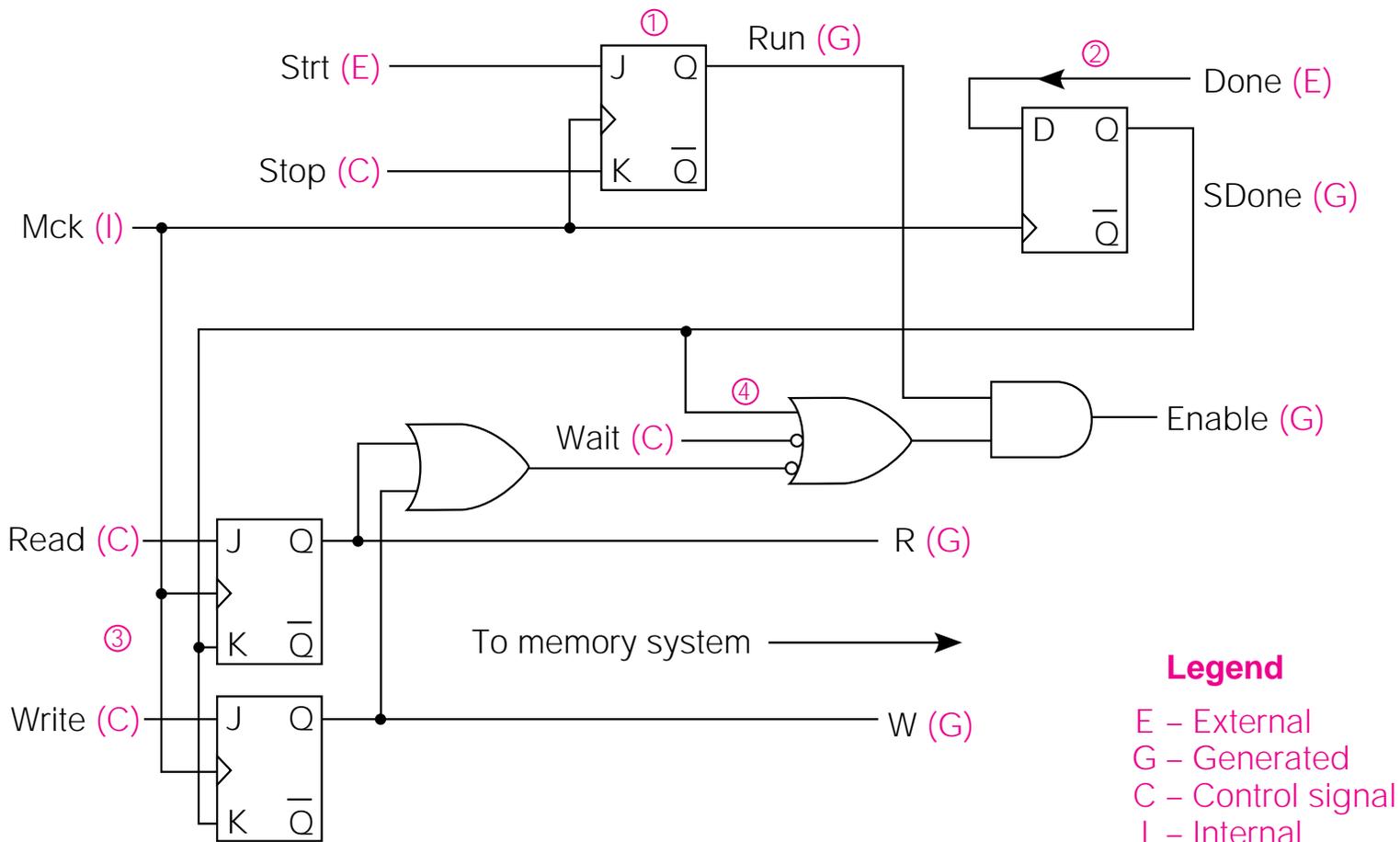


Fig 4.14 The Clocking Logic: Start, Stop, and Memory Synchronization



- **Mck is master clock oscillator**