

Fig 4.1 Block Diagram of 1-Bus SRC

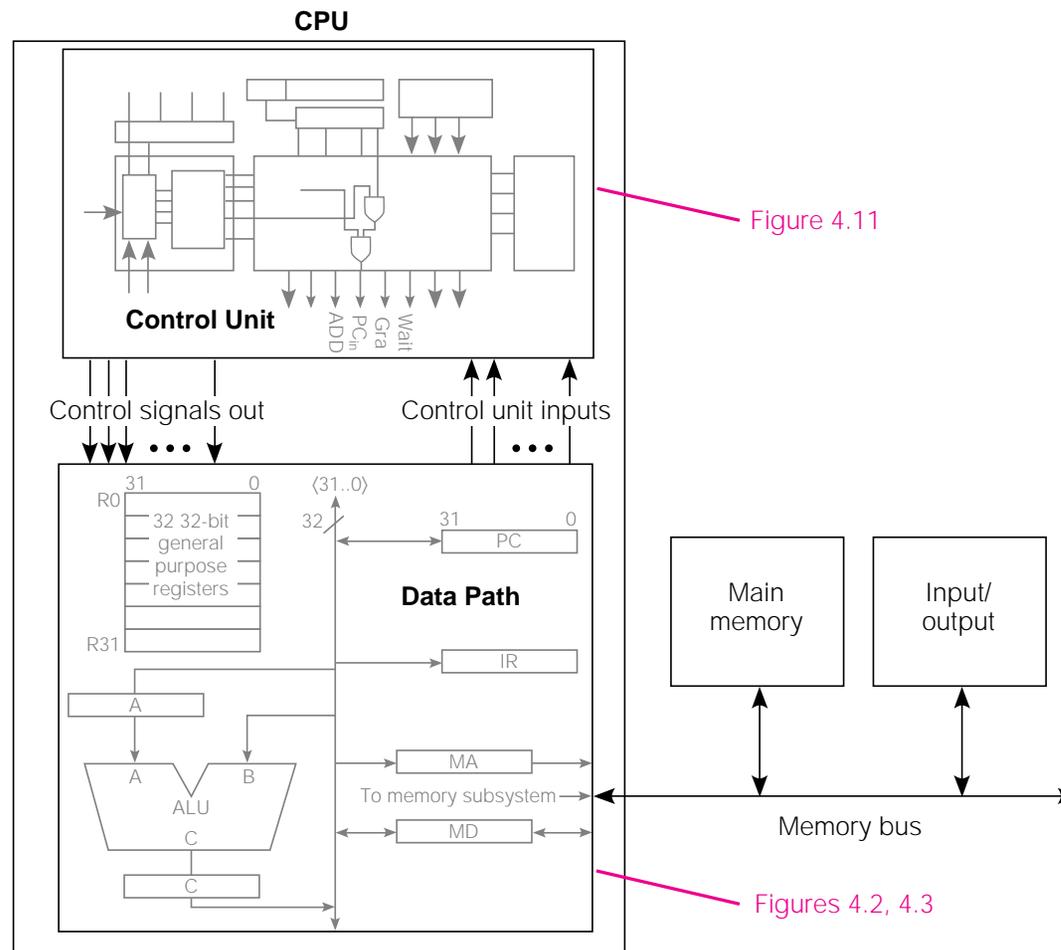
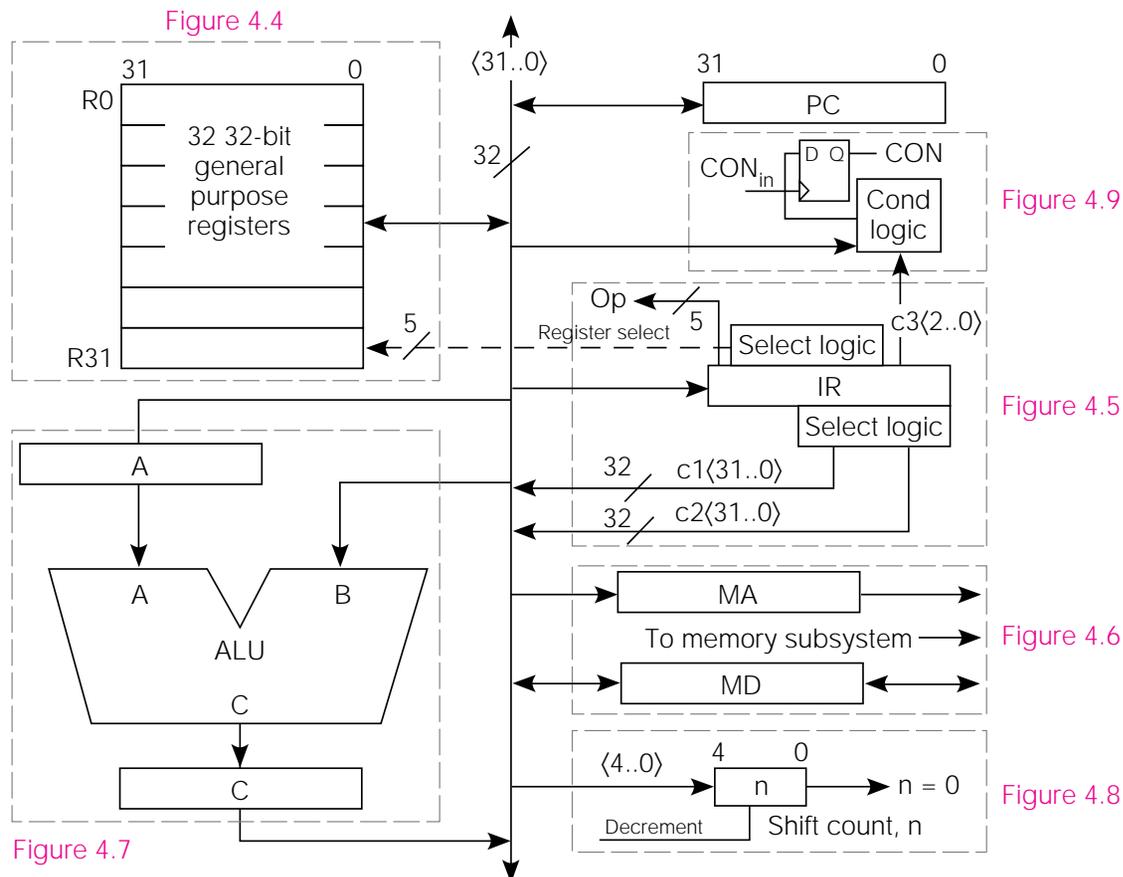


Fig 4.3 More Complete View of Registers and Buses in the 1-Bus SRC Design, Including Some Control Signals



- **Concrete RTN lets us add detail to the data path**

- **Instruction register logic and new paths**
- **Condition bit flip-flop**
- **Shift count register**

Keep this slide in mind as we discuss concrete RTN of instructions.