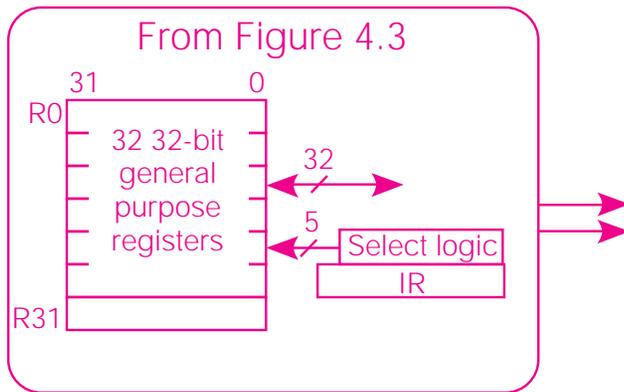


Fig 4.4 The Register File and Its Control Signals

- R_{out} gates selected register onto bus
- R_{in} strobed selected register from bus



- BA_{out} differs from R_{out} by gating 0 when R[0] is selected

BA = Base Address →

