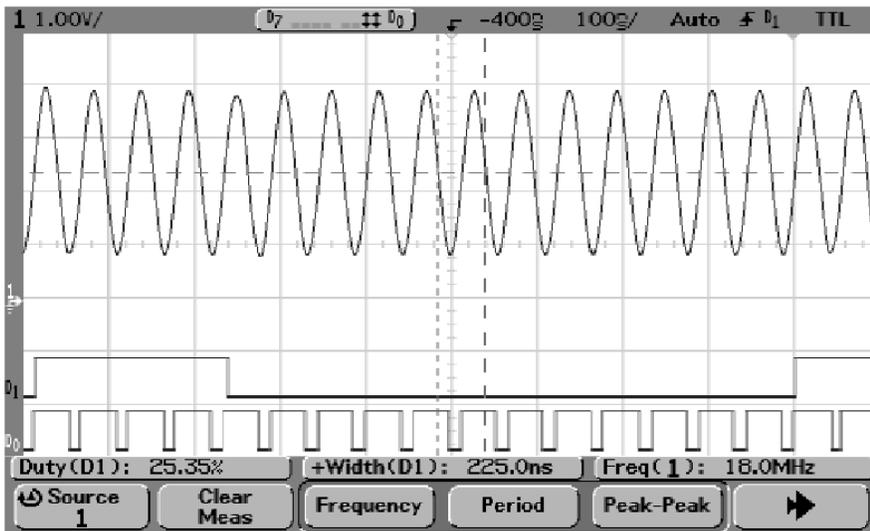
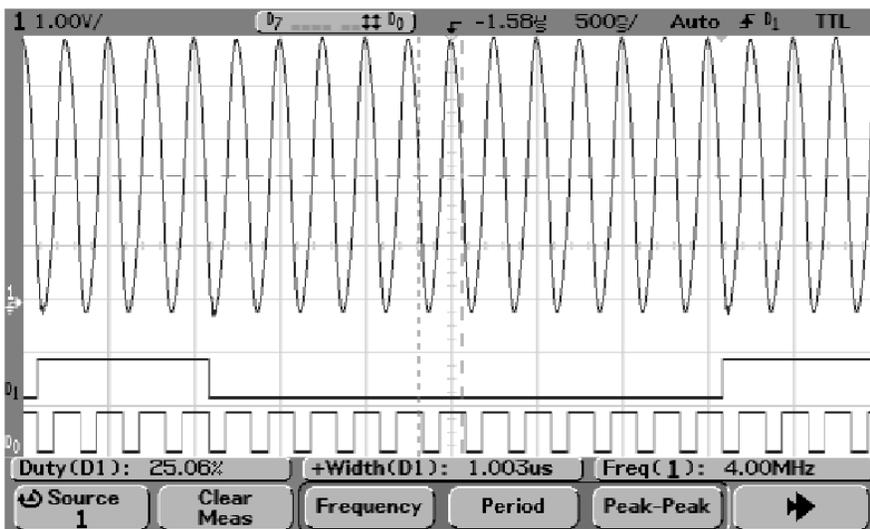


The graphs below show the behavior of the test874.asm program. In each case, signal 1 is the sinusoid;  $D_0$  is the same as signal 1 except that it is displayed by the oscilloscope as a digital signal rather than as an analog signal; and  $D_1$  is the output of Port A, Bit 0, found at Pin 2 of the PIC16F874.

At the bottom of each plot are shown the duty cycle of  $D_1$ , the width of the positive portion of  $D_1$ , corresponding to the duration of one instruction cycle; and the frequency of the timing signal.



Duty Cycle = 25.35%  
Instruction Cycle = 225.0 ns  
 $f_{osc} = 18.0$  MHz



Duty Cycle = 25.06%  
Instruction Cycle = 1.003  $\mu$ s  
 $f_{osc} = 4.00$  MHz