

Table 2.9

Conditional branch penalties considering PC-relative addressing and scheduling of penalty slot

PC-relative Addressing	Schedulable	Branch Penalty
Yes (90%)	Yes (50%)	0 cycles
Yes (90%)	No (50%)	1 cycle
No (10%)	Yes (50%)	1 cycle
No (10%)	No (50%)	2 cycles

Table 2.9: Conditional branch penalties considering PC-relative addressing and scheduling penalty slot.