

Dynamic instruction mix of the benchmark set\*

<b>Instruction Mix</b>	<b>Integer Benchmarks (SPECInt92)</b>				<b>Floating-Point Benchmarks (SPECfp92)</b>		
	<i>compress</i>	<i>eqntott</i>	<i>espresso</i>	<i>li</i>	<i>alvinn</i>	<i>hydro2d</i>	<i>tomcatv</i>
Integer							
Arithmetic (single cycle)	42.73	48.79	48.30	29.54	37.50	26.25	19.93
Arithmetic (multicycle)	0.89	1.26	1.25	5.14	0.29	1.19	0.05
Load	25.39	23.21	24.34	28.48	0.25	0.46	0.31
Store	16.49	6.26	8.29	18.60	0.20	0.19	0.29
Floating-point							
Arithmetic (pipelined)	0.00	0.00	0.00	0.00	12.27	26.99	37.82
Arithmetic (nonpipelined)	0.00	0.00	0.00	0.00	0.08	1.87	0.70
Load	0.00	0.00	0.00	0.01	26.85	22.53	27.84
Store	0.00	0.00	0.00	0.01	12.02	7.74	9.09
Branch							
Unconditional	1.90	1.87	1.52	3.26	0.15	0.10	0.01
Conditional	12.15	17.43	15.26	12.01	10.37	12.50	3.92
Conditional to count register	0.00	0.44	0.10	0.39	0.00	0.16	0.05
Conditional to link register	4.44	0.74	0.94	2.55	0.03	0.01	0.00

\*Values given are percentages.

Table 6.1: Dynamic instruction mix of the benchmark set. Values given are percentages.