Table 6.7
Frequency of dispatch stall cycles*

Sources of Dispatch Stalls	*compress*	*eqntott*	*espresso*	*li*	*alvinn*	*hydro2d*	*tomcatv*
Serialization	0.00	0.00	0.00	0.00	0.00	0.00	0.00
Move to special register constraint	0.00	4.49	0.94	3.44	0.00	0.95	0.08
Read port saturation	0.26	0.00	0.02	0.00	0.32	2.23	6.73
Reservation station saturation	36.07	22.36	31.50	34.40	22.81	42.70	36.51
Rename buffer saturation	24.06	7.60	13.93	17.26	1.36	16.98	34.13
Completion buffer saturation	5.54	3.64	2.02	4.27	21.12	7.80	9.03
Another to same unit	9.72	20.51	18.31	10.57	24.30	12.01	7.17
No dispatch stalls	24.35	41.40	33.28	30.06	30.09	17.33	6.35

*Values given are percentages.

Table 6.7 Frequency of dispatch stall cycles.
(Values given are percentages.)