

Table 6.9

Average execution latency (in cycles) in each of the six execution units for the benchmarks

Execution Units	<i>compress</i>	<i>eqntott</i>	<i>espresso</i>	<i>li</i>	<i>alvinn</i>	<i>hydro2d</i>	<i>tomcatv</i>
XSU0 (1 stage)	1.53	1.62	1.89	2.28	1.05	1.48	1.01
XSU1 (1 stage)	1.72	1.73	2.23	2.39	1.13	1.78	1.03
MC-FXU (>1 stage)	4.35	4.82	6.18	5.64	3.48	9.61	1.64
FPU (3 stages)	—*	—*	—*	—*	5.29	6.74	4.45
LSU (2 stages)	3.56	2.35	2.87	3.22	2.39	2.92	2.75
BRU (≥ 1 stage)	2.71	2.86	3.11	3.28	1.04	4.42	4.14

*Very few instructions are executed in this unit for these benchmarks.

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