

EE332
Assignment 30
Digital Logic

Name: _____
Section: 3311

1. Use a Karnaugh map to generate minimal logic for a function F which will be true (1) if its 4-bit input is in the range 7 to 11 (inclusive) or if it is evenly divisible by 4.
2. Manipulate the result so that it is implemented using 2-input NAND gates only. Draw a schematic for the resultant equation.