

Figure 1: Analysis of a JK-Flip-flop-based Synchronous Counter

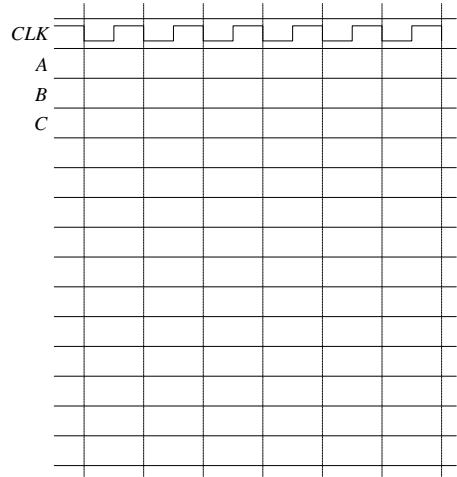


Figure 2: Timing Analysis